



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Koji Iketani et al.
 Serial No. :
 Filed : July 6, 2001
 Title : SEMICONDUCTOR DEVICE MANUFACTURING METHOD

Art Unit : Unknown
 Examiner : Unknown

S. Stevenson
P3
10-3-01

Commissioner for Patents
 Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Applicant submits the reference listed on the attached form PTO-1449, a copy of which is enclosed. The relevance of the reference is discussed on page 2 of the present specification.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 6, 2001

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